



C. J. C.

PATENT
Attorney's Doc. No. 4591-306

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent No.: US 6,819,600 B2
Inventor: Sim
Issued: November 16, 2004

For: SEMICONDUCTOR MEMORY DEVICE WITH OFFSET-COMPENSATED
SENSING SCHEME

Ser. No.: 10/611,788
Group Art Unit: 2818

Filed: June 30, 2003
Examiner: M. Tran

TRANSMITTAL LETTER

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Certificate
DEC 07 2005
of Correction

Enclosed for filing are the following:

- ☒ Request for Certificate of Correction
- ☒ Certificate of Correction Form PTO 1050
- ☒ Return postcard
- ☒ Any deficiency or overpayment should be charged or credited to deposit account number 13-1703.

Respectfully submitted,

MARGER JOHNSON & McCOLLOM, P.C.

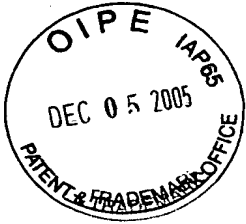
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REQUEST FOR CERTIFICATE OF CORRECTION

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Alexandria, VA 22313-1450

Upon comparison of the above-identified original Letters Patent with applicant's copy of the application therefore, the following error in the original Letters Patent has been noted:

At Column 16, Line 67, please replace "has a, first input" with --has a first input--

Applicant respectfully requests that a Certificate of Correction calling attention to the above error be issued pursuant to Rule 322 of the Patent Office and Title 35, United States Code, Section 254. Two copies of a Certificate of Correction form are enclosed.

Respectfully submitted,

MARGER JOHNSON & McCOLLOM, P.C.

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UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO : US 6,819,600 B2
DATED : November 16, 2004
INVENTOR(S) : Sim

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

At Column 16, Line 67, please replace "has a, first input" with --has a first input--

MAILING ADDRESS OF SENDER:
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210 S.W. Morrison Street, Suite 400
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PATENT NO. US 6,819,600 B2

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